

ABSTRACT OF THE DISCLOSURE

Authentication circuits 2-3 to 2-11 are provided between
a debug I/F circuit 2-1 and a debug terminal. The
5 authentication circuit transmits a transmission key to
externally at the time of activation, and authenticates from
a received signal and the transmission key, and enables to
access a debug I/F. It is possible to prevent a spurious access
from the debug I/F by a third person by the authentication
10 circuit.

auth. circuit 2-3 to 2-11
debug I/F circuit 2-1
debug terminal
transmission key
activation
received signal
spurious access
third person